

IN THE SPECIFICATION

Please amend paragraphs [0008], [0009], and [0010] of the specification as follows:

[0008] In one aspect of the invention, an electronic device includes an electronic component and an integrated circuit configured to generate a system clock [[and]] an external clock and/or feedback clock having a programmable delay from the system clock, the integrated circuit being further configured to provide the external clock to the electronic component to support communications therewith, communicate with the electronic component, and calibrate the external and/or feedback clock delay as a function of the communications. The term component represents one or more devices with synchronous interfaces physically residing in one or more than one packages.

[0009] In another aspect of the present invention, a method of calibrating an integrated circuit to an electronic component, the integrated circuit having a system clock. The method includes generating an external and/or feedback clock on the integrated circuit, the [external] clocks having a programmable delay from the system clock, providing the external clock from the integrated circuit to the electronic component to support communications therewith, communicating between the integrated circuit and the electronic component, and calibrating the external and/or feedback clock delay as a function of the communications.

[0010] In yet another aspect of the present invention, an electronic device includes an electronic component, and an integrated circuit. The integrated circuit includes means for generating a system clock, means for generating an external and/or feedback clock having a programmable delay from the system clock, means for providing the external clock to the electronic component to support communications therewith, means for communicating between the integrated circuit and the electronic component, and means for calibrating the external and/or feedback clock delay as a function of the communications.